Wideband Compact PI Equivalent Circuit for Modeling On-Chip Spiral Inductors

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Abstract—A novel wideband nine-element PI equivalent circuit is proposed for modeling on-chip deep-submicron spiral inductors. Mathematical formulations derived by analyzing three quality factors enable fast and accurate extraction of all model elements as frequency-independent values. The model was confirmed using three spiral inductors that were realized in a 90 nm CMOS process and compared with other models to demonstrate its superior accuracy.

Index Terms—Equivalent circuit model, quality factor, SPICEcompatible, spiral inductor.

I. INTRODUCTION

ANY complex and difficult circuit analysis and design tasks are performed with CAD tools, which require accurate component models to achieve accurate circuit simulation results. Currently, the most common nine-element PI model of inductors [1] is narrow-banded. Available bandwidth in the model can be increased by using relatively more complex model configurations such as a modified T configuration [2], [3] or a two-PI configuration [4]. A simpler approach is increasing the number of elements in the PI model [4]–[7]. However, as elements are increased, model parameter extraction becomes more difficult and complex, potentially requiring time-consuming optimization. Modeling methods based on electromagnetic (EM) simulation have a large computational load [5]. Moreover, the resultant frequency-dependent physical models are difficult to integrate into SPICE-compatible simulators.

The elements in the wideband compact model proposed in this study (Fig. 1(a)) are identical to those in the PI model [1] except for an R_p element representing high-frequency loss and an L_g - C_g - R_g resonator representing higher-order transmission line effects on substrate impedance. The two-step model extraction procedure exploits three Q factors to develop extraction formulations of model elements without optimization. The model can be integrated into SPICE-compatible simulators such that the values of its elements are independent of frequency. The proposed model was validated using CMOS spiral inductors with an octagonal layout, as shown in Fig. 1(b).

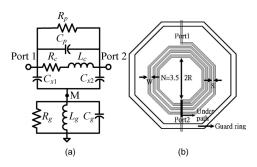


Fig. 1. Equivalent model for a 90 nm CMOS spiral inductor. (a) Proposed wideband equivalent circuit. (b) Layout of an octagonal shaped spiral inductor.

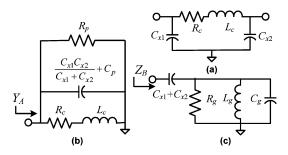


Fig. 2. Simplified and decomposed circuits. (a) The PI-equivalent circuit at low frequency. (b) The decomposed equivalent circuit with input admittance of Y_A . (c) The decomposed equivalent circuit with input impedance of Z_B .

II. MODEL EXTRACTION PROCEDURE

A. Step 1 Extraction at Low Frequency Condition

When operating at a frequency much lower than either of the two resonant frequencies of L_c , C_p and L_g , $(C_{x1}+C_{x2})$, the proposed model can be simplified as a simple PI model, as shown in Fig. 2(a). After the two-port measured S parameters are converted into Z and Y parameters, the four elements in Fig. 2(a) can be determined from the following admittance parameters:

$$C_{x1} = \omega^{-1} \operatorname{Im} \{ Y_{11} + Y_{12} \}, \text{ at low frequency}$$
(1)

$$C_{x2} = \omega^{-1} \text{Im} \{ Y_{22} + Y_{12} \}, \text{ at low frequency}$$
 (2)

$$Z_c = R_c + j\omega L_c = -\frac{1}{Y_{12}}, \text{ at low frequency.}$$
(3)

B. Step 2 Extraction at High Frequency Condition

The proposed model uses elements other than the above four elements to capture high frequency effects [6]. To simplify the extraction of these elements, the proposed model is decomposed

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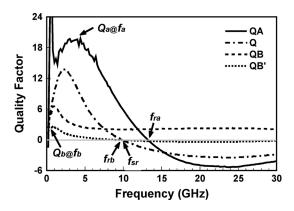


Fig. 3. Frequency responses of Q, Q_A , Q_B and Q'_B factors for the 5 nH CMOS spiral inductor.

according to the following relations into two one-port equivalent circuits [2], [3]:

$$Y_{A} = Z_{A}^{-1} = (Z_{11} + Z_{22} - 2Z_{12})^{-1}$$

= $\frac{1}{R_{c} + j\omega L_{c}} + j\omega C_{p} + \frac{1}{R_{p}} + j\omega \left(\frac{C_{x1}C_{x2}}{C_{x1} + C_{x2}}\right)$ (4)
 $Z_{B} = Y_{B}^{-1} = (Y_{11} + Y_{22} + 2Y_{12})^{-1}$
= $\left(j\omega C_{g} + \frac{1}{j\omega L_{g}} + \frac{1}{R_{g}}\right)^{-1} + \frac{1}{j\omega (C_{x1} + C_{x2})}$. (5)

Fig. 2(b) and (c) present the two one-port equivalent circuits with an input admittance of Y_A and an input impedance of Z_B , respectively. By relating Fig. 1(a) to Fig. 2(b) and (c), Y_A can be considered the input admittance as seen when looking into port 1 with an open circuit in node M and a short circuit from port 2 to the ground, and Z_B can be considered the input impedance as seen when looking into port 1 with a short circuit from port 2 to port 1. With knowledge of the four elements L_c , R_c , C_{x1} and C_{x2} , which were extracted from Step 1, the remaining five elements, C_p , R_p , L_g , R_g , and C_g , can be obtained using Y_A and Z_B from Step 2.

Another critical extraction procedure involves three Q factors of an inductor, which are defined as

$$Q(\omega) = -\frac{\operatorname{Im}\{Y_{11}(\omega)\}}{\operatorname{Re}\{Y_{11}(\omega)\}}$$
(6)

$$Q_A(\omega) = -\frac{\operatorname{Im}\{Y_A(\omega)\}}{\operatorname{Re}\{Y_A(\omega)\}}$$
(7)

$$Q_B(\omega) = -\frac{\operatorname{Im}\{Y_B(\omega)\}}{\operatorname{Re}\{Y_B(\omega)\}}.$$
(8)

Fig. 3 presents the frequency responses of the three Q factors for a 5 nH CMOS spiral inductor. The self-resonant frequencies $(f_{ra}, f_{rb}, \text{ and } f_{sr})$ are determined from the zero positions of the Q factors to extract the reactive elements. At the angular frequency $\omega_{ra} = 2\pi f_{ra}$, the imaginary part of (1) is zero, and solving this condition for C_p yields

$$C_p = \frac{1}{\omega_{ra}^2 L_c} - \left(\frac{C_{x1}C_{x2}}{C_{x1} + C_{x2}}\right).$$
 (9)

TABLE I GEOMETRICAL PARAMETERS AND THE EXTRACTED MODEL PARAMETERS FOR THE THREE 90 NM CMOS SPIRAL INDUCTORS

Geometrical and model parameters	5-nH inductor	3.3-nH inductor	1.5-nH inductor
W (μm)	6	9	3
S (μm)	3	3	3
Number of turns	3.5	3.5	3.5
Inner diameter (µm)	90	60	15
$\overline{L_c}$ (nH)	4.98	3.20	1.51
R_c (Ω)	3.05	1.84	2.87
C_p (fF)	3.67	3.55	0.97
R_{v} (Ω)	2736	2172	2316
C_{x1} (fF)	52.5	48.2	15.2
C_{x2} (fF)	58.9	56.5	25.1
L_g (nH)	2.19	2.51	Not needed
$\dot{C_g}$ (fF)	107	116	Not needed
R_{g} (Ω)	55.1	52.6	Not needed

The admittance parameter Y_{11} of the two-port inductor model neglecting the loss is

$$Y_{11} = \frac{1}{j\omega L_c} + j\omega \left(C_p + \frac{C_{x1}C_{x2}}{C_{x1} + C_{x2}} \right) + \frac{\frac{\omega C_{x1}^2 Y_B'}{C_{x1} + C_{x2}}}{(\omega (C_{x1} + C_{x2}) - jY_B')} \quad (10)$$

where

$$Y'_B = j\omega C_g + \frac{1}{j\omega L_g}.$$
(11)

The imaginary part of (10) vanishes at the angular frequency $\omega_{sr} = 2\pi f_{sr}$, and so

$$L_{g} = \frac{(\omega_{sr}^{2} - \omega_{rb}^{2})(\omega_{sr}^{2}L_{c}(C_{x1} + C_{p}) - 1)}{\omega_{sr}^{2}\omega_{rb}^{2}(C_{x1} + C_{x2} - \omega_{sr}^{2}L_{c}(C_{x1}C_{x2} + C_{p}(C_{x1} + C_{x2})))}.$$
(12)

Since $\omega_{rb} = 2\pi f_{rb}$ is the angular resonant frequency of the L_q - R_q - C_q resonator, C_q can be further determined using

$$C_g = \frac{1}{\omega_{rb}^2 L_g}.$$
(13)

With respect to the resistive elements, the peak Q_A factor value Q_a at the angular frequency $\omega_a = 2\pi f_a$ is used to extract R_p as follows:

$$R_{p} = \frac{Q_{a}(R_{c}^{2} + \omega_{a}^{2}L_{c}^{2})}{Q_{a}R_{c} + \omega_{a}L_{c} - \omega_{a}(R_{c}^{2} + \omega_{a}^{2}L_{c}^{2})\left(C_{p} + \frac{C_{x1}C_{x2}}{C_{x1} + C_{x2}}\right)}.$$
 (14)

In Fig. 3, Q'_B is defined as the quality factor of the L_g - C_g - R_g resonator. Similarly, the peak Q'_B factor value Q_b at the angular frequency $\omega_b = 2\pi f_b$ is used to extract R_g as follows:

$$R_g = \frac{Q_b \omega_b L_g}{1 - \omega_b^2 C_g L_g}.$$
(15)

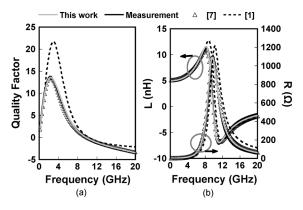


Fig. 4. Comparison of (a) quality factor and (b) series inductance and resistance, between measurement and the proposed model against the models studied in [1] and [7] for the 5 nH 90 nm CMOS spiral inductor.

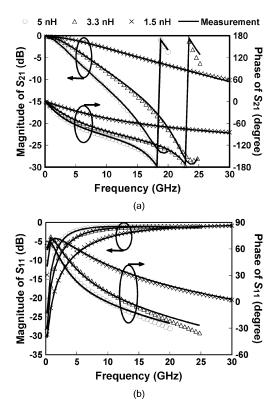


Fig. 5. Measured and modeled results for the magnitude of S-parameters, where black lines are measured results and symbols are modeled results. (a) S_{21} (b) S_{11} .

III. MODEL VERIFICATION AND DISCUSSION

Table I lists the layout parameters and the model parameters extracted using the proposed method for the three spiral inductors realized in a 90 nm single-poly nine-metal (1P9M) CMOS process. The substrate is 300 μ m thick silicon with a high resistivity of 0.1 Ω m. Above the substrate is 0.35 μ m thick field oxide. The metal layers are copper. The inductor uses the top

metal layer with a thickness of 3.3 μ m for winding and the second top metal layer with a thickness of 0.8 μ m for an underpath. The inter-metal dielectric (IMD) is 10 μ m thick with a dielectric constant of 4.2. Notably, the high-resistivity silicon substrate makes the internal inductance associated with the silicon substrate nearly invariant with frequency. Therefore, the extracted inductances in the model can be considered frequency independent over a wide frequency range [8].

To compare the proposed model with other reported models and measurement, Fig. 4(a) and (b) present the modeled Qfactor and the extracted inductance and resistance, respectively, of the 5 nH CMOS inductor. Clearly, the nine-element PI model described by Lee [1] is less accurate compared to the 12-element model presented by Chen [7]. The proposed nine- element model achieves comparable or even better performance mainly because the L_g - C_g - R_g resonator functions as the additive term for a third-order approximation of the transmission-line effects on the substrate impedance. Notably, a similar method devised by Lee [6] established a wideband equivalent circuit for the intrinsic inductor impedance. Based on the values extracted for the elements presented in Table I, the modeled and measured S-parameters in Fig. 5 confirm the accuracy of the proposed model.

IV. CONCLUSION

The proposed model uses only nine elements, which is the same number used in the conventional PI model, but it achieves relatively higher wideband accuracy. Good agreement between measured and modeled results validates the proposed model.

REFERENCES

- T. H. Lee, The Design of CMOS Radio-Frequency Integrated Circuits. New York: Cambridge Univ. Press, 1988.
- [2] T.-S. Horng, J.-M. Wu, L.-Q. Yang, and S.-T. Fang, "A novel modified-T equivalent circuit for modeling LTCC embedded inductors with a large bandwidth," *IEEE Trans. Microw. Theory Tech.*, vol. 51, no. 12, pp. 2327–2333, Dec. 2003.
- [3] Y.-S. Tsai, H.-W. Chou, Y.-C. Lin, and T.-S. Horng, "Accurate modeling of RF passive component in deep submicron process," in *Proc. Int. Conf. Appl. Electromag. Student Innovation Competition Awards*, 2010, pp. 167–171.
- [4] I. C. H. Lai and M. Fujishima, "A new on-chip substrate-coupled inductor model implemented with scalable expressions," *IEEE J. Solid-State Circuits*, vol. 41, no. 11, pp. 2491–2499, Nov. 2006.
- [5] J. Brinkhoff, K. S. S. Koh, K. Kang, and F. Lin, "Scalable transmission line and inductor models for CMOS millimeter-wave design," *IEEE Trans. Microw. Theory Tech.*, vol. 56, no. 12, pp. 2954–2962, Dec. 2008.
- [6] K.-Y. Lee, S. Mohammadi, P. K. Bhattacharya, and L. P. B. Katehi, "A wideband compact model for integrated inductors," *IEEE Microw. Wireless Compon. Lett.*, vol. 16, no. 9, pp. 490–492, Sep. 2006.
- [7] H.-H. Chen, H.-W. Zhang, S.-J. Chung, J.-T. Kuo, and T.-C. Wu, "Accurate systematic model-parameter extraction for on-chip spiral inductors," *IEEE Trans. Microw. Theory Tech.*, vol. 55, no. 9, pp. 3267–3273, Sep. 2008.
- [8] H. Ymeri, B. Nauwelaers, K. Maex, D. De Roest, M. Stucchi, and S. Vandenberghe, "Frequency-dependent internal inductance associated with silicon substrate of on-chip interconnects," in *Proc. Int. Sci. Conf. (RADIOELEKTRONIKA)*, Bratislava, Slovak Republic, 2002, pp. 14–16.